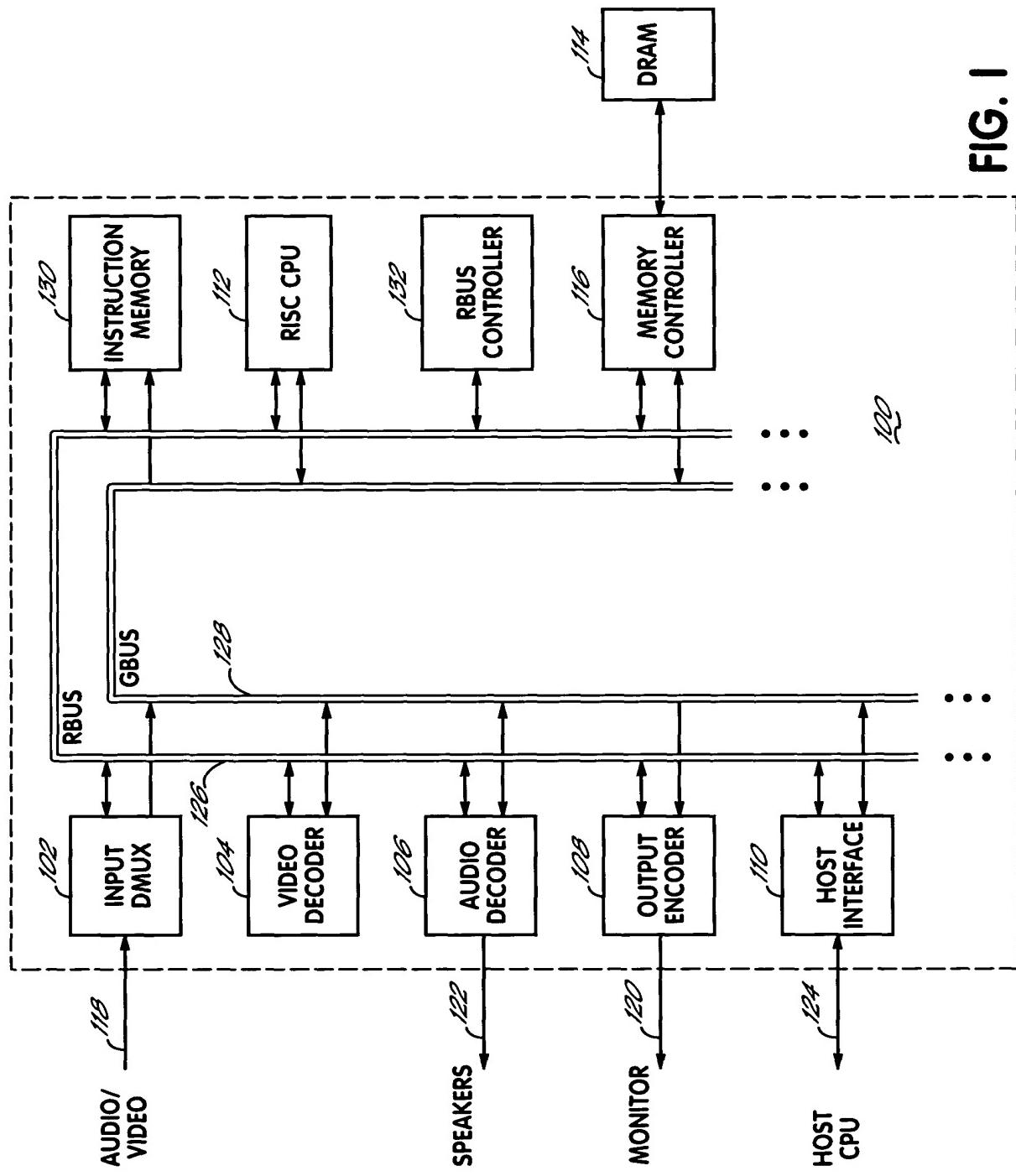


**FIG. 1**



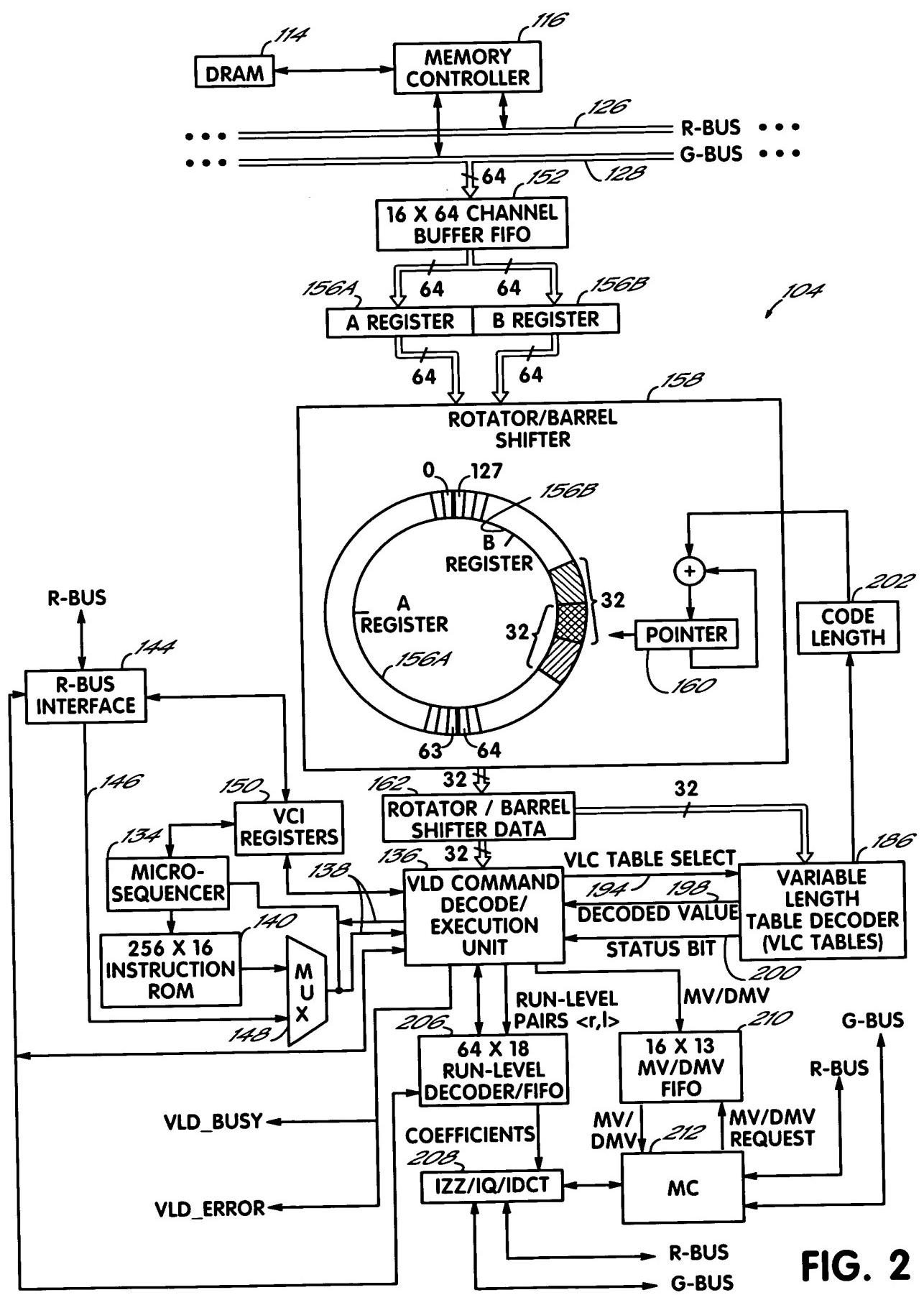
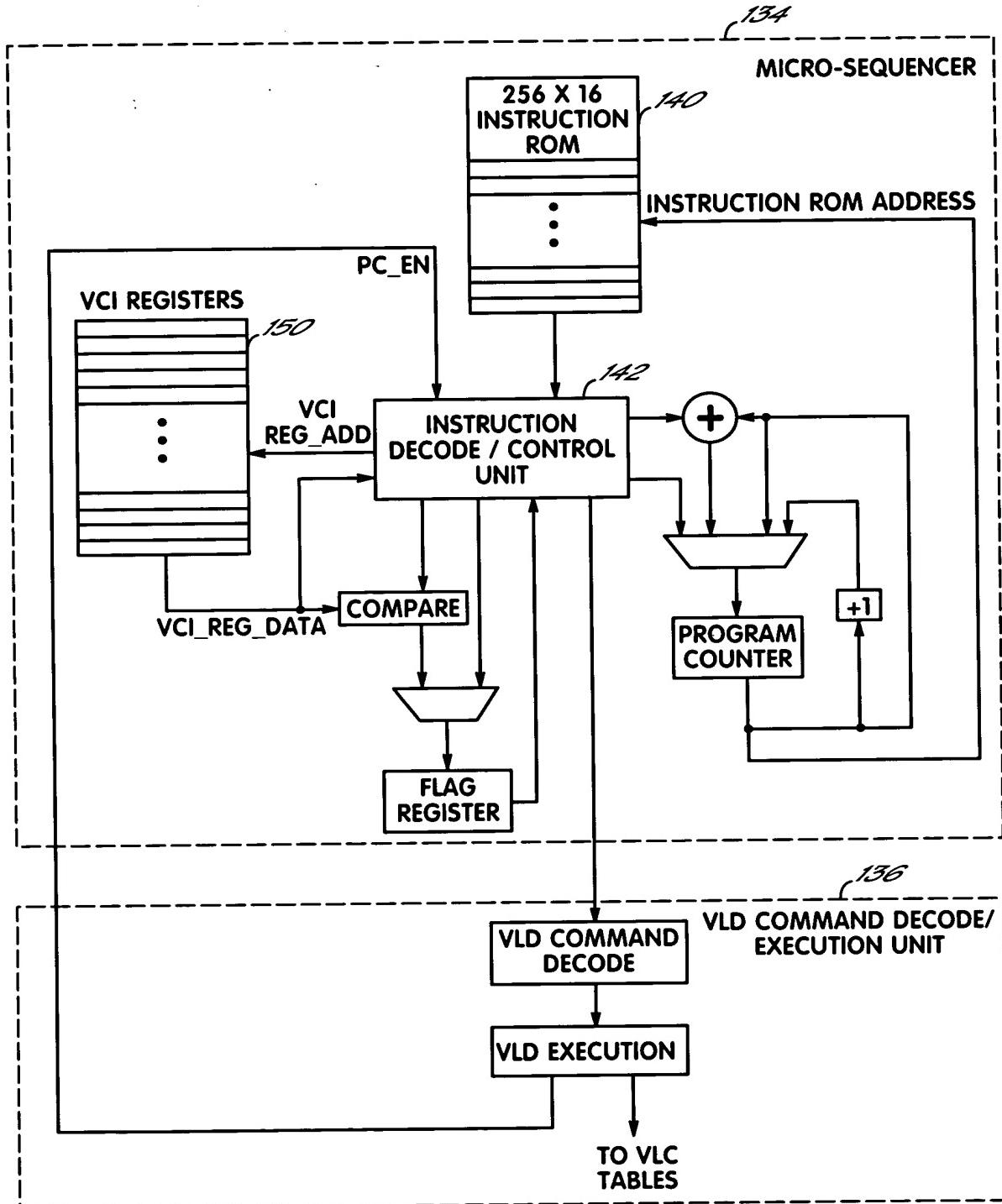
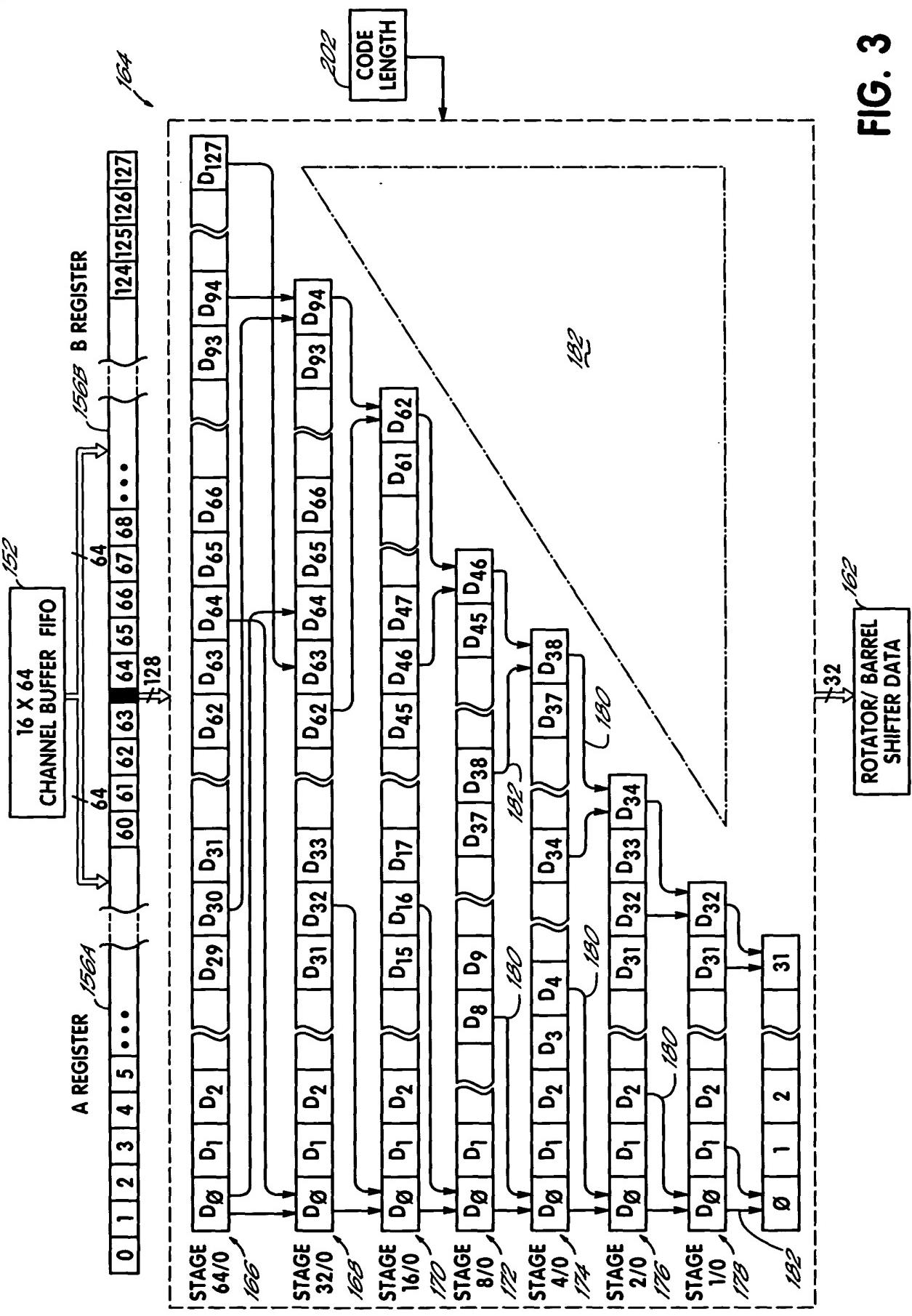


FIG. 2



**FIG. 2A**

FIG. 3

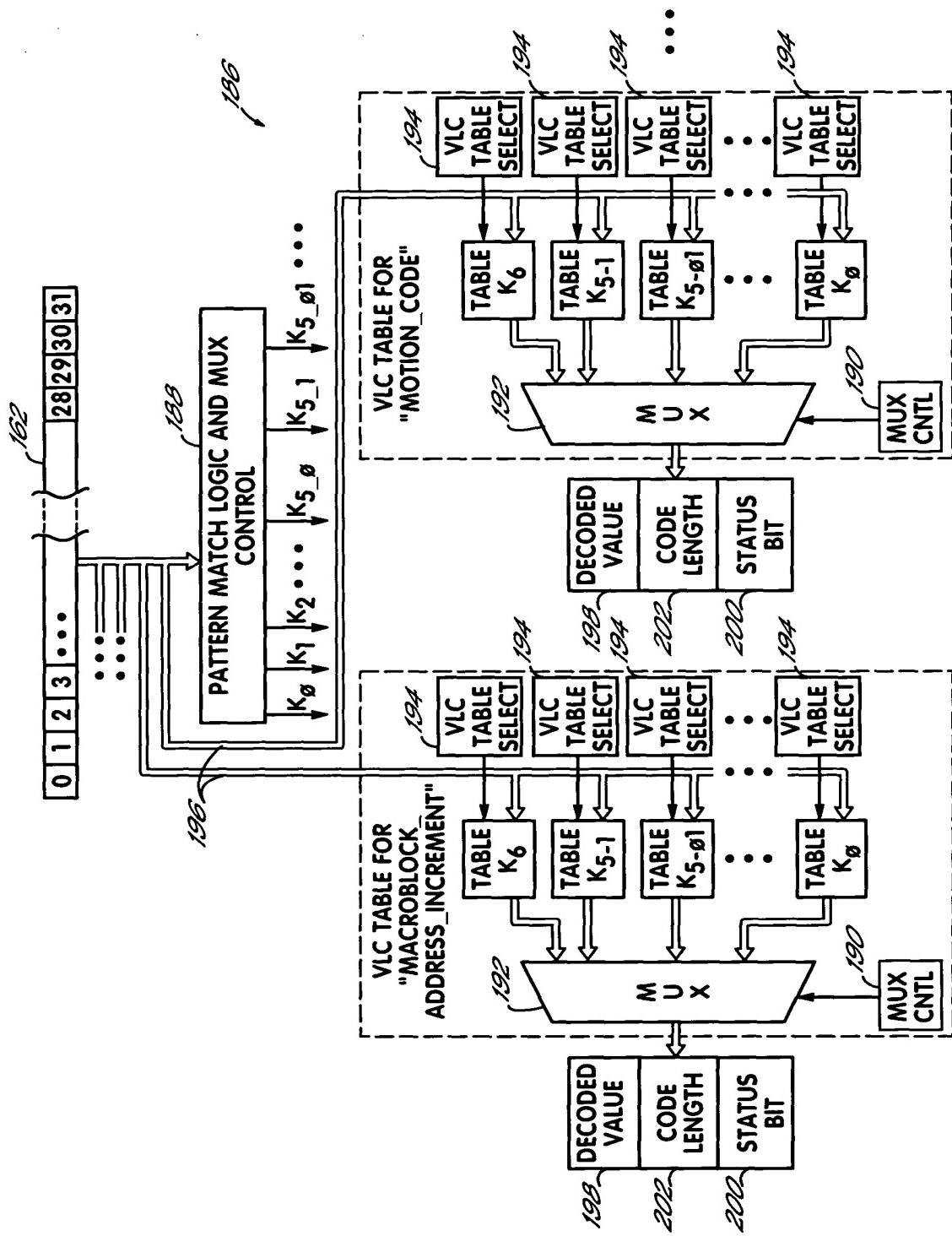


**PATTERN MATCH**  
**VLC TABLE FOR "MACROBLOCK\_ADDRESS\_INCREMENT"**

| VARIABLE LENGTH CODE  | INCREMENT VALUE | PATTERN MATCH      | CODE LENGTH |
|-----------------------|-----------------|--------------------|-------------|
| 1                     | 1               | K <sub>Ø</sub>     | 1           |
| 0 1 1                 | 2               | K <sub>1</sub>     | 3           |
| 0 1 0                 | 3               | K <sub>1</sub>     | 3           |
| 0 0 1 1               | 4               | K <sub>2</sub>     | 4           |
| 0 0 1 0               | 5               | K <sub>2</sub>     | 4           |
| 0 0 0 1 1             | 6               | K <sub>3</sub>     | 5           |
| 0 0 0 1 0             | 7               | K <sub>3</sub>     | 5           |
| 0 0 0 0 1 1 1         | 8               | K <sub>4_1</sub>   | 7           |
| 0 0 0 0 1 1 0         | 9               | K <sub>4_1</sub>   | 7           |
| 0 0 0 0 1 0 1 1       | 10              | K <sub>4_0</sub>   | 8           |
| 0 0 0 0 1 0 1 0       | 11              | K <sub>4_0</sub>   | 8           |
| 0 0 0 0 1 0 0 1       | 12              | K <sub>4_0</sub>   | 8           |
| 0 0 0 0 1 0 0 0       | 13              | K <sub>4_0</sub>   | 8           |
| 0 0 0 0 0 1 1 1       | 14              | K <sub>5_1</sub>   | 8           |
| 0 0 0 0 0 1 1 0       | 15              | K <sub>5_1</sub>   | 8           |
| 0 0 0 0 0 1 0 1 1 1   | 16              | K <sub>5_Ø1</sub>  | 10          |
| 0 0 0 0 0 1 0 1 1 0   | 17              | K <sub>5_Ø1</sub>  | 10          |
| 0 0 0 0 0 1 0 1 0 1   | 18              | K <sub>5_Ø1</sub>  | 10          |
| 0 0 0 0 0 1 0 1 0 0   | 19              | K <sub>5_Ø1</sub>  | 10          |
| 0 0 0 0 0 1 0 0 1 1   | 20              | K <sub>5_ØØ1</sub> | 10          |
| 0 0 0 0 0 1 0 0 1 0   | 21              | K <sub>5_ØØ1</sub> | 10          |
| 0 0 0 0 0 1 0 0 0 1 1 | 22              | K <sub>5_ØØØ</sub> | 11          |
| 0 0 0 0 0 1 0 0 0 1 0 | 23              | K <sub>5_ØØØ</sub> | 11          |
| 0 0 0 0 0 1 0 0 0 0 1 | 24              | K <sub>5_ØØØ</sub> | 11          |
| 0 0 0 0 0 1 0 0 0 0 0 | 25              | K <sub>5_ØØØ</sub> | 11          |
| 0 0 0 0 0 0 1 1 1 1 1 | 26              | K <sub>6</sub>     | 11          |
| 0 0 0 0 0 0 1 1 1 1 0 | 27              | K <sub>6</sub>     | 11          |
| 0 0 0 0 0 0 1 1 1 0 1 | 28              | K <sub>6</sub>     | 11          |
| 0 0 0 0 0 0 1 1 1 0 0 | 29              | K <sub>6</sub>     | 11          |
| 0 0 0 0 0 0 1 1 0 1 1 | 30              | K <sub>6</sub>     | 11          |
| 0 0 0 0 0 0 1 1 0 1 0 | 31              | K <sub>6</sub>     | 11          |
| 0 0 0 0 0 0 1 1 0 0 1 | 32              | K <sub>6</sub>     | 11          |
| 0 0 0 0 0 0 1 1 0 0 0 | 33              | K <sub>6</sub>     | 11          |
| 0 0 0 0 0 0 0 1 0 0 0 |                 | MACROBLOCK_ESCAPE  |             |

**FIG. 4**

FIG. 5



TBIT:

| OPCODE<br>3 | VCI REGISTER<br>5 | BITNUM<br>3 | VALUE<br>1 | 4-BIT FORWARD RELATIVE ADDRESS<br>4 |
|-------------|-------------------|-------------|------------|-------------------------------------|
|-------------|-------------------|-------------|------------|-------------------------------------|

214d 216 220 218 222

FIG. 6A

ICMD, CMDI:

| OPCODE<br>3 | VCI REGISTER<br>5 | VLD COMMAND<br>8 |
|-------------|-------------------|------------------|
|-------------|-------------------|------------------|

214d 216 244

FIG. 6B

COMP:

| OPCODE<br>3 | ST_CODE<br>1 | MBS<br>1 | FLAG<br>3 | IMMEDIATE DATA<br>8 |
|-------------|--------------|----------|-----------|---------------------|
|-------------|--------------|----------|-----------|---------------------|

214d 216 230 224 228 226

FIG. 6C

BRANCH:

| OPCODE<br>3 | UNUSED<br>3 | ERR<br>1 | HALT<br>1 | 8-BIT ABSOLUTE BRANCH ADDRESS<br>8 |
|-------------|-------------|----------|-----------|------------------------------------|
|-------------|-------------|----------|-----------|------------------------------------|

214d 216 234 236 232

FIG. 6D

SETF:

| OPCODE<br>3 | UNUSED<br>5 | IMMEDIATE DATA<br>8 |
|-------------|-------------|---------------------|
|-------------|-------------|---------------------|

214d 216 238

FIG. 6E

INCM:

| OPCODE<br>3 | VCI REGISTER<br>5 | IMMEDIATE DATA<br>8 |
|-------------|-------------------|---------------------|
|-------------|-------------------|---------------------|

214f 216 240 242

FIG. 6F

HALT:

| OPCODE<br>3 | UNUSED<br>5 | UNUSED<br>8 |
|-------------|-------------|-------------|
|-------------|-------------|-------------|

214g 216

FIG. 6G